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Database: US Patents Full-Text Database	▼

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(39 AND 20).USPT.

## **Search History**

DB Name	<u>Query</u>	Hit Count	Set Name
USPT .	120 and 139	2	<u>L46</u>
USPT	122 and 139	0	<u>L45</u>
USPT	129 and 139	0	<u>L44</u>
USPT	(11 or 12 or 13 or 16) and 14 and 15 and 139	3	<u>L43</u>
USPT	(11 or 12 or 13) and (14 or 15) and 139	22	<u>L42</u>
USPT	(11 or 12 or 13 or 14 or 15 or 16) and 139	86	<u>L41</u>
USPT	(133 or 134) and (135 or 136) and (137 or 138)	0	<u>L40</u>
USPT	(133 or 134 or 135 or 136) and (137 or 138)	86	<u>L39</u>
USPT	(phase or vernier or clock or edge) .ab.	212672	<u>L38</u>
USPT	(phase or vernier or clock or edge) .ti.	22765	<u>L37</u>
USPT	(timing difference) .ti.	3	<u>L36</u>
USPT	(timing difference) .ab.	54	<u>L35</u>
USPT '	(synchronous memory) .ab.	65	<u>L34</u>
USPT	(synchronous memory) .ti.	42	<u>L33</u>
USPT	(11 or 12 or 13) and (14 or 15) and 129	4	<u>L32</u>
USPT	(11 or 12 or 13 or 16) and 14 and 15 and 129	0	<u>L31</u>
USPT	(11 or 12 or 13 or 14 or 15 or 16) and 129	20	<u>L30</u>

	·		
USPT	synchronous and memory and clock and phase and leading and edge and difference and controller and vernier and adjust	22	<u>L29</u>
USPT	synchronous and memory and clock and phase and leading and edge and difference and controller	1053	<u>L28</u>
USPT	synchronous and memory and clock and phase and leading and edge	2265	<u>L27</u>
USPT	114 and 122	2	<u>L26</u>
USPT	(11 or 12 or 13 or 16) and 14 and 15 and 122	15	<u>L25</u>
USPT	(11 or 12 or 13 or 14 or 15 or 16) and 122	51	<u>L24</u>
USPT	(11 or 12 or 13 or 14 or 15 or 16) and 121	282	<u>L23</u>
USPT	. 115 and 116 and 118 and (117 and 119)	51	<u>L22</u>
USPT	115 and 116 and 118 and (117 or 119)	287	<u>L21</u>
USPT	115 and 116 and 118	809	<u>L20</u>
USPT	synchronous same memory	7617	<u>L19</u>
USPT	memory same controller	54409	<u>L18</u>
USPT	timing same difference	19316	<u>L17</u>
USPT	phase same (detector or comparing or shift or decoder)	84647	<u>L16</u>
USPT	leading same edge	99398	<u>L15</u>
USPT	(11 or 13 or 16) and (14 or 15) and 12	40	<u>L14</u>
USPT	(11 or 13 or 16) and (14 or 15)	436	<u>L13</u>
USPT	11 and 16	0	<u>L12</u>
USPT	11 and 13	6	<u>L11</u>
USPT	. 12 and 13 and (11 or 16)	1	<u>L10</u>
USPT	14 and 15 and (11 or 12 or 13 or 16)	115	<u>L9</u>
USPT	12 and 14 and 15 and (11 or 13 or 16)	1	<u>L8</u>
USPT	12 and 14 and 15	56	<u>L7</u>
USPT	vernier and echo	95	<u>L6</u>
USPT	leading edge	76316	<u>L5</u>
USPT	phase adj (detector or comparing or shift or decoder)	41660	<u>L4</u>
USPT	timing difference	1118	<u>L3</u>
USPT	memory controller	8152	<u>L2</u>
USPT	synchronous memory	365	<u>L1</u>



Help Logout

Main Menu | Search Form | Posting Counts | Show S Numbers | Edit S Numbers

Generate Collection

## Search Results - Record(s) 1 through 1 of 1 returned.

1. Document ID: US 5946244 A

Entry 1 of 1

File: USPT

Aug 31, 1999

US-PAT-NO: 5946244

DOCUMENT-IDENTIFIER: US 5946244 A

TITLE: Delay-locked loop with binary-coupled capacitor

Full Title Citation Front Review Classification Date Reference Claims KWC Image

 Term
 Documents

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 1

Display 10 Documents

including document number

1

Display Format: TI

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Main Menu | Search Form | Posting Counts | Show S Numbers | Edit S Numbers | Help | Logout

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Main Menu | Search Form | Posting Counts | Show S Numbers | Edit S Numbers

Generate Collection

## Search Results - Record(s) 1 through 1 of 1 returned.

1. Document ID: US 5426756 A

Entry 1 of 1

File: USPT

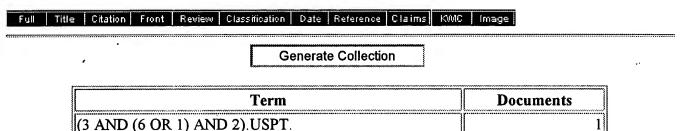
Jun 20, 1995

US-PAT-NO: 5426756

DOCUMENT-IDENTIFIER: US 5426756 A

TITLE: Memory controller and method determining empty/full status of a FIFO memory

using gray code counters



Display 10 Documents including document number 1

Display Format: TI Change Format

Main Menu | Search Form | Posting Counts | Show S Numbers | Edit S Numbers

Help Logout

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## WEST

Help Logout

Main Menu | Search Form | Posting Counts

Show S Numbers

Edit S Numbers

## **Generate Collection**

## Search Results - Record(s) 1 through 6 of 6 returned.

1. Document ID: US 5930197 A

Entry 1 of 6

File: USPT

Jul 27, 1999

US-PAT-NO: 5930197

DOCUMENT-IDENTIFIER: US 5930197 A TITLE: Semiconductor memory device

## Full Title Citation Front Review Classification Date Reference Claims KMC Image

2. Document ID: US 5889416 A

Entry 2 of 6

File: USPT

Mar 30, 1999

US-PAT-NO: 5889416

DOCUMENT-IDENTIFIER: US 5889416 A TITLE: Symmetrical nand gates

## Full Title Citation Front Review Classification Date Reference Claims KWC Image

3. Document ID: US 5740115 A

Entry 3 of 6

File: USPT

Apr 14, 1998

US-PAT-NO: 5740115

DOCUMENT-IDENTIFIER: US 5740115 A TITLE: Semiconductor memory device

## Full Title Citation Front Review Classification Date Reference Claims KMC Image

4. Document ID: US 5699003 A

Entry 4 of 6

File: USPT

Dec 16, 1997

US-PAT-NO: 5699003

DOCUMENT-IDENTIFIER: US 5699003 A

TITLE: Delay circuit device

## Full Title Citation Front Review Classification Date Reference Claims KMC Image

5. Document ID: US 5530894 A

Entry 5 of 6

File: USPT

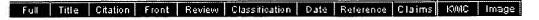
Jun 25, 1996

US-PAT-NO: 5530894

DOCUMENT-IDENTIFIER: US 5530894 A

TITLE: Adapter for interfacing a computer to a multichannel digital network, with

port for a telephone



6. Document ID: US 5426756 A

Entry 6 of 6

File: USPT

Jun 20, 1995

US-PAT-NO: 5426756

DOCUMENT-IDENTIFIER: US 5426756 A

TITLE: Memory controller and method determining empty/full status of a FIFO memory

using gray code counters



### **Generate Collection**

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Main Menu | Search Form | Posting Counts | Show S Numbers | Edit S Numbers | Help | Logout

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Search Form Main Menu

**Posting Counts** 

Show S Numbers

Edit S Numbers

**Generate Collection** 

Search Results - Record(s) 1 through 10 of 15 returned.

☐ 1. Document ID: US 5663942 A

Entry 1 of 15

File: USPT

Sep 2, 1997

US-PAT-NO: 5663942

DOCUMENT-IDENTIFIER: US 5663942 A

TITLE: Jitter measurement apparatus detecting amplitude of phase errors between

information signal and synchronized clock signal

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KMC | Image |

2. Document ID: US 5291494 A

Entry 2 of 15

File: USPT

Mar 1, 1994

US-PAT-NO: 5291494

DOCUMENT-IDENTIFIER: US 5291494 A

TITLE: Method of handling errors in software

Full Title Citation Front Review Classification Date Reference Claims KWC Image

3. Document ID: US 5251227 A

Entry 3 of 15

File: USPT

Oct 5, 1993

US-PAT-NO: 5251227

DOCUMENT-IDENTIFIER: US 5251227 A

TITLE: Targeted resets in a data processor including a trace memory to store

transactions

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWC | Image |

4. Document ID: US 5185877 A

Entry 4 of 15

File: USPT

Feb 9, 1993

US-PAT-NO: 5185877

DOCUMENT-IDENTIFIER: US 5185877 A

TITLE: Protocol for transfer of DMA data

Full Title Citation Front Review Classification Date Reference Claims KWC Image

5. Document ID: US 5163138 A

Entry 5 of 15

File: USPT

US-PAT-NO: 5163138

DOCUMENT-IDENTIFIER: US 5163138 A

TITLE: Protocol for read write transfers via switching logic by transmitting and

retransmitting an address

Full Title Citation Front Review Classification Date Reference Claims KMC Image

6. Document ID: US 5153881 A

Entry 6 of 15

File: USPT

US-PAT-NO: 5153881

DOCUMENT-IDENTIFIER: US 5153881 A

TITLE: Method of handling errors in software

Full Title Citation Front Review Classification Date Reference Claims KMC Image

7. Document ID: US 5068851 A

Entry 7 of 15

File: USPT

US-PAT-NO: 5068851

DOCUMENT-IDENTIFIER: US 5068851 A

TITLE: Apparatus and method for documenting faults in computing modules

Full Title Citation Front Review Classification Date Reference Claims KMC Image

8. Document ID: US 5068780 A

Entry 8 of 15

File: USPT

US-PAT-NO: 5068780

DOCUMENT-IDENTIFIER: US 5068780 A

TITLE: Method and apparatus for controlling initiation of bootstrap loading of an operating system in a computer system having first and second discrete computing

zones

Full Title Citation Front Review Classification Date Reference Claims KMC Image

9. Document ID: US 5065312 A

Entry 9 of 15

File: USPT

US-PAT-NO: 5065312

DOCUMENT-IDENTIFIER: US 5065312 A

TITLE: Method of converting unique data to system data

Full Title Citation Front Review Classification Date Reference Claims KWC Image

10. Document ID: US 5048022 A

Entry 10 of 15

File: USPT

US-PAT-NO: 5048022

DOCUMENT-IDENTIFIER: US 5048022 A

TITLE: Memory device with transfer of ECC signals on time division multiplexed

bidirectional lines

Full Title Citation Front Review Classification Date Reference Claims KMC Image

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**Generate Collection** 

	Documents
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Display 10 Documents

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Display Format: TI

Change Format

Main Menu | Search Form | Posting Counts | Show S Numbers | Edit S Numbers

Help Logout

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Help Logout

Main Menu | Search Form | Posting Counts | Show S Numbers

Generate Collection

## Search Results - Record(s) 11 through 15 of 15 returned.

11. Document ID: US 5019906 A

Entry 11 of 15

File: USPT

May 28, 1991

Edit S Numbers

US-PAT-NO: 5019906

DOCUMENT-IDENTIFIER: US 5019906 A

TITLE: Time base corrector memory arrangement and memory control

Full Title Citation Front Review Classification Date Reference Claims KMC Image

12. Document ID: US 4763203 A

Entry 12 of 15

File: USPT

Aug 9, 1988

US-PAT-NO: 4763203

DOCUMENT-IDENTIFIER: US 4763203 A

TITLE: Time base corrector with accurate timing corrector control

Full Title Citation Front Review Classification Date Reference Claims KWIC Image

13. Document ID: US 4733294 A

Entry 13 of 15

File: USPT

Mar 22, 1988

US-PAT-NO: 4733294

DOCUMENT-IDENTIFIER: US 4733294 A

TITLE: Time base corrector memory arrangement and memory control

Full Title Citation Front Review Classification Date Reference Claims KMC Image

14. Document ID: US 4724495 A

Entry 14 of 15

File: USPT

Feb 9, 1988

US-PAT-NO: 4724495

DOCUMENT-IDENTIFIER: US 4724495 A

TITLE: Digital formatter, controller, and head assembly for video disk recording

system, and method

Full Title Citation Front Review Classification Date Reference Claims KMC Image

15. Document ID: US 4577240 A

Entry 15 of 15

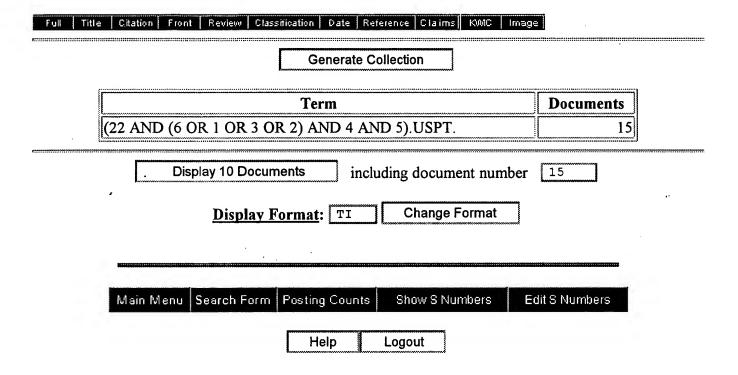
File: USPT

Mar 18, 1986

US-PAT-NO: 4577240

DOCUMENT-IDENTIFIER: US 4577240 A

TITLE: Multiple zone multiple disk video recording system



L26



Help Logout

Main Menu | Search Form | Posting Counts | Show S Numbers | Edit S Numbers

**Generate Collection** 

## Search Results - Record(s) 1 through 2 of 2 returned.

1. Document ID: US 5933623 A

Entry 1 of 2

File: USPT

Aug 3, 1999

US-PAT-NO: 5933623

DOCUMENT-IDENTIFIER: US 5933623 A

TITLE: Synchronous data transfer system

Full Title Citation Front Review Classification Date Reference Claims KWIC Image

2. Document ID: US 5589992 A

Entry 2 of 2

File: USPT

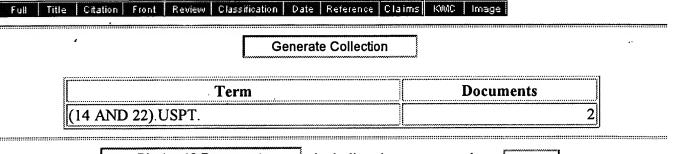
Dec 31, 1996

US-PAT-NO: 5589992

DOCUMENT-IDENTIFIER: US 5589992 A

TITLE: Magnetic recording and reproducing apparatus for performing slow-motion

reproduction with higher quality of image



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Display Format: TI

**Change Format** 

Main Menu Search Form Posting Counts Show S Numbers Edit S Numbers

Help Logout

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Help Logout

Posting Counts

Show S Numbers

Edit S Numbers

**Generate Collection** 

Search Results - Record(s) 1 through 10 of 20 returned.

1. Document ID: US 5430660 A

Entry 1 of 20

File: USPT

Jul 4, 1995

US-PAT-NO: 5430660

DOCUMENT-IDENTIFIER: US 5430660 A TITLE: Digital pulse generator

Main Menu | Search Form

Title Citation Front Review Classification Date Reference Claims KMC Image

2. Document ID: US 5422760 A

Entry 2 of 20

File: USPT

Jun 6, 1995

US-PAT-NO: 5422760

DOCUMENT-IDENTIFIER: US 5422760 A

TITLE: Disk drive method using zoned data recording and PRML sampling data

detection with digital adaptive equalization

Title Citation Front Review Classification Date Reference Claims KMC Image

3. Document ID: US 5416648 A

Entry 3 of 20

File: USPT

May 16, 1995

US-PAT-NO: 5416648

DOCUMENT-IDENTIFIER: US 5416648 A

TITLE: Masterless synchronized spindle control for hard disk drives

Full Title Citation Front Review Classification Date Reference Claims KWC Image

4. Document ID: US 5375145 A

Entry 4 of 20

File: USPT

Dec 20, 1994

US-PAT-NO: 5375145

DOCUMENT-IDENTIFIER: US 5375145 A

TITLE: Multi-mode gain control loop for PRML class IV sampling data detection

channel

Full Title Citation Front Review Classification Date Reference Claims KWC Image

5. Document ID: US 5345342 A

Entry 5 of 20

File: USPT

Sep 6, 1994

US-PAT-NO: 5345342

DOCUMENT-IDENTIFIER: US 5345342 A

TITLE: Disk drive using PRML synchronous sampling data detection and asynchronous

detection of embedded sector servo

Full Title Citation Front Review Classification Date Reference Claims KWIC Image

6. Document ID: US 5341249 A

Entry 6 of 20

File: USPT

Aug 23, 1994

US-PAT-NO: 5341249

DOCUMENT-IDENTIFIER: US 5341249 A

TITLE: Disk drive using PRML class IV sampling data detection with digital

adaptive equalization

Full Title Citation Front Review Classification Date Reference Claims KWIC Image

7. Document ID: US 5252977 A

Entry 7 of 20

File: USPT

Oct 12, 1993

US-PAT-NO: 5252977

DOCUMENT-IDENTIFIER: US 5252977 A

TITLE: Digital pulse generator using digital slivers and analog vernier increments

Full Title Citation Front Review Classification Date Reference Claims KWIC Image

8. Document ID: US 5249132 A

Entry 8 of 20

File: USPT

Sep 28, 1993

US-PAT-NO: 5249132

DOCUMENT-IDENTIFIER: US 5249132 A TITLE: Digital pulse generator

Full Title Citation Front Review Classification Date Reference Claims KWIC Image

9. Document ID: US 5224129 A

Entry 9 of 20

File: USPT

Jun 29, 1993

US-PAT-NO: 5224129

DOCUMENT-IDENTIFIER: US 5224129 A

TITLE: Method of synchronizing signals of a pulse generator

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Image |

10. Document ID: US 5208598 A

Entry 10 of 20

File: USPT

May 4, 1993

US-PAT-NO: 5208598

DOCUMENT-IDENTIFIER: US 5208598 A

TITLE: Digital pulse generator using leading and trailing edge placement

Full Title Citation Front Review Classification Date Reference Claims KWC Image

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130

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		cluding document numl	ber [11]
Main Menu	Search Form Posting Coun	ts Show S Numbers	Edit S Numbers

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WEST

Help Logout

Main Menu Search Form Posting Counts

Show S Numbers

Edit S Numbers

**Generate Collection** 

Search Results - Record(s) 11 through 20 of 20 returned.

11. Document ID: US 4752884 A

Entry 11 of 20

File: USPT

Jun 21, 1988

US-PAT-NO: 4752884

DOCUMENT-IDENTIFIER: US 4752884 A

TITLE: Precision platform pointing controller for a dual-spin spacecraft

Full Title Citation Front Review Classification Date Reference Claims KMC Image

12. Document ID: US 4618897 A

Entry 12 of 20

File: USPT

Oct 21, 1986

US-PAT-NO: 4618897

DOCUMENT-IDENTIFIER: US 4618897 A

TITLE: System for synchronizing plural data storage devices to a common master

Full Title Citation Front Review Classification Date Reference Claims KWC Image

13. Document ID: US 4561443 A

Entry 13 of 20

File: USPT

Dec 31, 1985

US-PAT-NO: 4561443

DOCUMENT-IDENTIFIER: US 4561443 A

TITLE: Coherent inductive communications link for biomedical applications

Full Title Citation Front Review Classification Date Reference Claims KMC Image

14. Document ID: US 4267458 A

Entry 14 of 20

File: USPT

May 12, 1981

US-PAT-NO: 4267458

DOCUMENT-IDENTIFIER: US 4267458 A

TITLE: System and method for starting, synchronizing and operating a steam turbine

with digital computer control

Full Title Citation Front Review Classification Date Reference Claims KMC Image

15. Document ID: US 4065212 A

Entry 15 of 20

File: USPT

Dec 27, 1977

US-PAT-NO: 4065212

DOCUMENT-IDENTIFIER: US 4065212 A

TITLE: Inspection tool

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KMC | Image

16. Document ID: US 4040748 A

Entry 16 of 20

File: USPT

Aug 9, 1977

US-PAT-NO: 4040748

DOCUMENT-IDENTIFIER: US 4040748 A

TITLE: Inspection tool

Full Title Citation Front Review Classification Date Reference Claims KWC Image

17. Document ID: US 4040745 A

Entry 17 of 20

File: USPT

Aug 9, 1977

US-PAT-NO: 4040745

DOCUMENT-IDENTIFIER: US 4040745 A

TITLE: Inspection tool

Full Title Citation Front Review Classification Date Reference Claims KWC Image

18. Document ID: US 4037971 A

Entry 18 of 20

File: USPT

Jul 26, 1977

US-PAT-NO: 4037971

DOCUMENT-IDENTIFIER: US 4037971 A

TITLE: Inspection tool

Full Title Citation Front Review Classification Date Reference Claims KWC Image

19. Document ID: US 4037941 A

Entry 19 of 20

File: USPT

Jul 26, 1977

US-PAT-NO: 4037941

DOCUMENT-IDENTIFIER: US 4037941 A

TITLE: Inspection tool

Full Title Citation Front Review Classification Date Reference Claims KWC Image

20. Document ID: US 4029255 A

Entry 20 of 20

File: USPT

Jun 14, 1977

US-PAT-NO: 4029255

DOCUMENT-IDENTIFIER: US 4029255 A

TITLE: System for operating a steam turbine with bumpless digital megawatt and

impulse pressure control loop switching

Full Title Citation Front Review Classification Date Reference Claims KMC Image

**Generate Collection** 

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Help Logout

Main Menu | Search Form | Posting Counts | Sh

Show S Numbers | Edit S Numbers

**Generate Collection** 

Search Results - Record(s) 1 through 4 of 4 returned.

1. Document ID: US 5422760 A

Entry 1 of 4

File: USPT

Jun 6, 1995

US-PAT-NO: 5422760

DOCUMENT-IDENTIFIER: US 5422760 A

TITLE: Disk drive method using zoned data recording and PRML sampling data

detection with digital adaptive equalization

Full Title Citation Front Review Classification Date Reference Claims KMC Image

2. Document ID: US 5416648 A

Entry 2 of 4

File: USPT

May 16, 1995

US-PAT-NO: 5416648

DOCUMENT-IDENTIFIER: US 5416648 A

TITLE: Masterless synchronized spindle control for hard disk drives

Full Title Citation Front Review Classification Date Reference Claims KWIC Image

3. Document ID: US 5345342 A

Entry 3 of 4

File: USPT

Sep 6, 1994

US-PAT-NO: 5345342

DOCUMENT-IDENTIFIER: US 5345342 A

TITLE: Disk drive using PRML synchronous sampling data detection and asynchronous

detection of embedded sector servo

Full Title Citation Front Review Classification Date Reference Claims KMC Image

4. Document ID: US 5341249 A

Entry 4 of 4

File: USPT

Aug 23, 1994

US-PAT-NO: 5341249

DOCUMENT-IDENTIFIER: US 5341249 A

TITLE: Disk drive using PRML class IV sampling data detection with digital

adaptive equalization

Full Title Citation Front Review Classification Date Reference Claims KMC Image

**Generate Collection** 

Logout

Help

Help Logout

Main Menu Search Form Posting Counts

Show S Numbers

Edit S Numbers

**Generate Collection** 

Search Results - Record(s) 1 through 3 of 3 returned.

1. Document ID: US 5903915 A

Entry 1 of 3

File: USPT

May 11, 1999

US-PAT-NO: 5903915

DOCUMENT-IDENTIFIER: US 5903915 A

TITLE: Cache detection using timing differences

Full Title Citation Front Review Classification Date Reference Claims KWC Image

2. Document ID: US 5737531 A

Entry 2 of 3

File: USPT

Apr 7, 1998

US-PAT-NO: 5737531

DOCUMENT-IDENTIFIER: US 5737531 A

TITLE: System for synchronizing by transmitting control packet to omit blocks from transmission, and transmitting second control packet when the timing difference exceeds second predetermined threshold

Full Title Citation Front Review Classification Date Reference Claims KWIC Image

3. Document ID: US 5506845 A

Entry 3 of 3

File: USPT

Apr 9, 1996

US-PAT-NO: 5506845

DOCUMENT-IDENTIFIER: US 5506845 A

TITLE: ISDN terminal adapter using reduced memory for timing difference

compensation

Full Title Citation Front Review Classification Date Reference Claims KMC Image

Generate Collection

Term	Documents
TIMING.USPT.	3520
TIMINGS.USPT.	19
DIFFERENCE.USPT.	725
DIFFERENCES.USPT.	183
(TIMING ADJ DIFFERENCE).TIUSPT.	. 3

Display 10 Documents including document number 3

Display Format: TI Change Format

Main Menu Search Form Posting Counts Show S Numbers Edit S Numbers

Help Logout

## L43 WEST

Help Logout

Main Menu | Search Form | Posting Counts | Show S Numbers | Edit S Numbers

**Generate Collection** 

## Search Results - Record(s) 1 through 3 of 3 returned.

1. Document ID: US 5832021 A

Entry 1 of 3

File: USPT

Nov 3, 1998

US-PAT-NO: 5832021

DOCUMENT-IDENTIFIER: US 5832021 A

TITLE: Correlation detector for use in a spread spectrum communications receiver

## Full Title Citation Front Review Classification Date Reference Claims KMC Image

2. Document ID: US 4338569 A

Entry 2 of 3

File: USPT

Jul 6, 1982

US-PAT-NO: 4338569

DOCUMENT-IDENTIFIER: US 4338569 A

TITLE: Delay lock loop

## Full Title Citation Front Review Classification Date Reference Claims KMC Image

3. Document ID: US 4309673 A

Entry 3 of 3

File: USPT

Jan 5, 1982

US-PAT-NO: 4309673

DOCUMENT-IDENTIFIER: US 4309673 A

TITLE: Delay lock loop modulator and demodulator

Full Title Citation Front Review Classification Date Reference Claims KWC Image

**Generate Collection** 

Term	Documents
(39 AND (6 OR 1 OR 3 OR 2) AND 4 AND 5).USPT.	3

Display 10 Documents

including document number 3

3

Display Format: TI

**Change Format** 

Help Logout

Posting Counts Show S Numbers Edit S Numbers Main Menu Search Form

**Generate Collection** 

## Search Results - Record(s) 1 through 2 of 2 returned.

1. Document ID: US 5883853 A

Entry 1 of 2

File: USPT

Mar 16, 1999

US-PAT-NO: 5883853

DOCUMENT-IDENTIFIER: US 5883853 A

TITLE: Clock frequency detector for a synchronous memory device

## Full Title Citation Front Review Classification Date Reference Claims KWC Image

2. Document ID: US 5784332 A

Entry 2 of 2

File: USPT

Jul 21, 1998

US-PAT-NO: 5784332

DOCUMENT-IDENTIFIER: US 5784332 A

TITLE: Clock frequency detector for a synchronous memory device

Citation Front Review Classification Date Reference Claims

**Generate Collection** 

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(39 AND 20).USPT.		2

**Display 10 Documents** 

including document number

Display Format: TI

**Change Format** 

Show S Numbers Edit S Numbers Main Menu Search Form Posting Counts Help Logout